Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended): A digital television system comprising:

a first and second housing;

a receiver~~, adapted~~ to receive a digital television signal[[,]] in said first housing;

a digital television display in said second housing; and

a digital graphics bus coupled to said receiver in said first housing and said display in said second housing to transmit processed video data in a digital format from said first housing to said second housing.

Claim 2 (currently amended): The system of claim 1 wherein said first housing is part of a modular platform ~~adapted~~ to receive replaceable cards.

Claim 3 (original): The system of claim 2 wherein each of said cards is received in a plug, said plugs for said cards coupled by a bus.

Claim 4 (currently amended): The system of claim 1 wherein said digital graphics bus is coupled to an encryption ~~and a decryption~~ engine ~~so that traffic~~ to encrypt the processed video data before it is transmitted across said digital graphics bus ~~may be encrypted~~.

Claim 5 (original): The system of claim 2 wherein one of said cards is a motherboard including a processor.

Claim 6 (original): The system of claim 5 wherein another of said cards is a television tuner/capture card.

Claim 7 (cancel)

Claim 8 (original): The system of claim 2 including plugs in said platform for both power and data.

Claim 9 (currently amended): The system of claim 8 wherein said plugs are ~~adapted~~ to receive two different types of serial bus interfaces.

Claim 10 (cancel)

Claim 11 (currently amended): An apparatus comprising:

an encryption engine in a digital television receiver coupled to a digital graphics bus to encrypt digital signals transferred from said digital television receiver to said digital graphics bus, said encryption engine to provide two different levels of encryption; and

a decryption engine in a digital television display coupled to said digital graphics bus to decrypt the digital signals transferred from said digital graphics bus to said digital television display.

Claim 12 (canceled)

Claim 13 (currently amended): The apparatus of claim 11 wherein said digital graphics bus is ~~adapted~~ to periodically encrypt at a higher level of encryption.

Claim 14 (currently amended): The apparatus of claim 13 wherein the level of encryption is ~~adapted~~ to change on frame boundaries.

Claim 15 (previously presented): The apparatus of claim 11 wherein said encryption and decryption engines include linear feedback shift registers.

Claim 16 (previously presented): The apparatus of claim 15 wherein said shift registers include programmable tap registers.

Claim 17 (currently amended): The apparatus of claim 16 wherein said programmable tap registers are ~~adapted~~ to receive external tap selection input signals.

Claim 18 (currently amended): The apparatus of claim 17 including a combiner ~~adapted~~ to combine a seed signal together with feedback from said programmable tap register to create an input signal to said linear feedback shift register.

Claim 19 (previously presented): The apparatus of claim 18 wherein said tap register includes combinatorial logic and tap memory.

Claim 20 (previously presented): The apparatus of claim 11 including a decryption and an encryption engine on both ends of said digital graphics bus.

Claim 21 (currently amended): The apparatus of claim 11 wherein said digital graphics bus is ~~adapted~~ to transfer streaming video at 100 megahertz or higher.

Claims 22-28 (cancel)

Claim 29 (currently amended): A method of implementing a digital television system comprising:

~~providing a receiver in a first housing for~~ receiving a digital television signal with a receiver in a first housing;

~~providing a display in a second housing coupled to said first housing;~~

transmitting encrypted video signals between said first housing and a second housing coupled to said first housing ~~housings~~, said second housing including a display; and

periodically changing the level of encryption of said encrypted video signals.

Claim 30 (original): The method of claim 29 wherein changing the level of encryption includes changing the level of encryption on frame boundaries.

Claim 31 (previously presented): The method of claim 29 further comprising transmitting the encrypted video signals via a digital graphics bus.

Claim 32 (new): The system of claim 1, wherein the digital graphics bus comprises a first transition minimized differential signaling (TDMS) link and a second TDMS link.

Claim 33 (new): The system of claim 32, wherein the first TDMS link is to transmit reduced blanking interval data.

Claim 34 (new): The apparatus of claim 11, wherein the digital graphics bus comprises a first transition minimized differential signaling (TDMS) link and a second TDMS link.

Claim 35 (new): The apparatus of claim 34, wherein the first TDMS link is to transmit reduced blanking interval data.